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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/681,728	10/08/2003	Robbie Thielemans	920522-94808	6705
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BARNES & THORNBURG			HODGES, MATTHEW P	
P.O. BOX 2786 CHICAGO, IL 60690-2786			ART UNIT	PAPER NUMBER
			2879	
			DATE MAILED: 08/08/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)		
Office Action Summary		.10/681,728	THIELEMANS ET AL.		
		Examiner	Art Unit		
		Matt P. Hodges	2879		
Period for	- The MAILING DATE of this communication app r Reply	pears on the cover sheet with the c	orrespondence address		
THE N - Extens after S - If the p - If NO p - Failure Any re	DRTENED STATUTORY PERIOD FOR REPLY MAILING DATE OF THIS COMMUNICATION. sions of time may be available under the provisions of 37 CFR 1.13 EX (6) MONTHS from the mailing date of this communication. period for reply specified above is less than thirty (30) days, a reply period for reply is specified above, the maximum statutory period we to reply within the set or extended period for reply will, by statute, ply received by the Office later than three months after the mailing dipatent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be tim y within the statutory minimum of thirty (30) days vill apply and will expire SIX (6) MONTHS from , cause the application to become ABANDONED	ely filed s will be considered timely. the mailing date of this communication. O (35 U.S.C. § 133).		
Status					
1)[Responsive to communication(s) filed on <u>08 O</u>	ctober 2003.			
	· · · · · · · · · · · · · · · · · · ·	action is non-final.			
3) 🗌 :	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.				
Dispositio	on of Claims		•		
5)□ (6)⊠ (7)□ (Claim(s) 1-16 is/are pending in the application. (a) Of the above claim(s) is/are withdray Claim(s) is/are allowed. Claim(s) 1-16 is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restriction and/or	vn from consideration.			
Application	on Papers	•	•		
10)⊠ T , ,	The specification is objected to by the Examine the drawing(s) filed on <u>08 October 2003</u> is/are: Applicant may not request that any objection to the office of the correction	a)⊠ accepted or b)⊡ objected drawing(s) be held in abeyance. See ion is required if the drawing(s) is obje	37 CFR 1.85(a). ected to. See 37 CFR 1.121(d).		
Priority ur	nder 35 U.S.C. § 119				
a)[⊠ 1 2 3	acknowledgment is made of a claim for foreign All b) Some * c) None of: Certified copies of the priority documents Copies of the certified copies of the priority documents Copies of the certified copies of the priorical purchase the attached detailed Office action for a list of	s have been received. s have been received in Application ity documents have been received (PCT Rule 17.2(a)).	on No d in this National Stage		
Attachment(:	s)				
1) Notice 2) Notice 3) Informa	of References Cited (PTO-892) of Draftsperson's Patent Drawing Review (PTO-948) ation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) No(s)/Mail Date 3/8/2004.	4) Interview Summary (Paper No(s)/Mail Dat 5) Notice of Informal Pa 6) Other:	te		

Art Unit: 2879

DETAILED ACTION

Specification

The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-3, 8, 9, 12, and 13 are rejected under 35 U.S.C. 102(b) as being anticipated by Curtin et al. (US 5,686,790).

Regarding claims 1, 2, 3, Curtin discloses (see figure 11) a display device including a substrate having an array of addressable light emitting devices, an input means, drive circuitry, electrical connections, and cooling channels (1101a). Drive circuitry is electrically connected to the light emitting devices by way of the electrical connections through the channel layer (1101b). (Column 26 lines 17-34).

Regarding claim 8, the devices are kept inside a controlled atmosphere or vacuum for among other reasons, protection from the environment.

Regarding claim 9, the front faceplate that encapsulates the devices is substantially transparent.

Regarding claim 12, the cooling channels are filled with a liquid or gas cooling fluid.

Art Unit: 2879

Regarding claim 13, the use of a liquid cooling fluid necessitates the use of a propulsion means for that liquid. Thus one would be inherent in the prior art.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1-2 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kusaka (US 6,201,346) in view of Geusic et al. (US 6,496,370).

Regarding claims 1-5, 7-13, Kusaka discloses (see figure 8) a display device including organic EL light emitting elements (2), drive circuitry (7 and 8), electrical connections (6), and an encapsulant (4). Kusaka further identifies the problem of heat build up on the back of the device between the light emitting elements and the drive circuitry. Kusaka does not appear to specify the use of a cooling channel system in the substrate, however, Geusic, in the same field of endeavor, disclose the use of a cooling channel system in a device substrate for cooling elements on either side of the substrate. Geusic discloses (see figure 1, 2e, and 4) the use of cooling channels (116) formed in the middle of the substrate, interconnects (220) formed between the cooling channels and insulated by layer (222), and high heat conductive substrate or heat sink (202) formed at the top of the channels. Channels are filled with a liquid and propelled by device 412. These channels advantageously allow for heat to be removed efficiently while not restricting the use of drive circuitry on the back of the substrate. Thus, it would have been

Art Unit: 2879

obvious at the time the invention was made to a person having ordinary skills in the art to incorporate the cooling channel system taught by Geusic into the device as disclosed Kusaka in order to advantageously allow for heat to be removed efficiently while not restricting the use of drive circuitry on the back of the substrate.

Regarding claim 6, Kusaka in view of Geusic discloses the device as claimed (see rejection of claim 1 above) but does not appear to specify the use of a semiconductive light emitting device as the light emitting element. However semiconductive EL devices are a well understood alternative to organic EL devices when the device requires additional environmental protection or to lower the cost of manufacture. Thus, it would have been obvious at the time the invention was made to a person having ordinary skills in the art to incorporate the use of a semiconductive light emitting device in the place of the organic EL elements in the device as disclosed Kusaka in view of Geusic in order to advantageously provide additional environmental protection and to lower the cost of manufacture.

Claims 14-16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kusaka (US 6,201,346) in view of Geusic et al. (US 6,496,370) and further in view of Chiba et al. (US 2003/0173891 A1).

Regarding claim 14, Kusaka in view of Geusic discloses the device as claimed (see rejection of claim 1 above) but does not appear to specify the use of the flat panel displays in a tiled display. However Chiba, in the same field of endeavor, discloses the assembling of organic flat panel displays into a tiled display. Using multiple flat panel elements to form a tiled display advantageously allows for an increase in display size. (Paragraph 0009). Thus, it would have

Art Unit: 2879

been obvious at the time the invention was made to a person having ordinary skills in the art to incorporate the use of tiling the flat panel displays into a tiled display as taught by Chiba with the device as disclosed Kusaka in view of Geusic in order to advantageously allow for an increase in display size.

Regarding claims 15 and 16, the tiling as taught by Chiba includes a small space between the display cells, therefore each panel is not directly connected to a neighboring panel and channels would still remain separate on each display panel. However Chiba further teaches the use of a plurality of panels, therefore meaning that at least some panels would be surrounded on all sides by neighboring panels. It has been recognized that one of ordinary skill in the art is expected to be able to make some modifications to the proposed combination in order to maintain functionality. In this case it would have been obvious, and required, to combine the cooling channels at some point outside of the panel, in order to feed the cooling material to central tiles.

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Lovgren et al. (US 5,159,529) discloses the use of a cooling system to reduce heat from a substrate.

Seshan et al. (US 6,377,457) discloses the use of a substrate including cooling channels to dissipate heat.

Art Unit: 2879

Contact Information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Matt P Hodges whose telephone number is (571) 272-2454. The examiner can normally be reached on 7:30 AM to 4:00 PM M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nimesh Patel can be reached on (571) 272-2457. The fax phone number for the organization where this application or proceeding is assigned is (571) 273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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PRIMARY EXAMINER